Optimal Irreducible Polynomials for GF(2^m) arithmetic

Michael Scott School of Computing Dublin City University

GF(2^m) polynomial representation

- A polynomial with coefficients either
 0 or 1 (*m* is a small prime)
- Stored as an array of bits, of length m, packed into computer words
- Addition (and subtraction) easy XOR. No reduction required as bit length does not increase.



- Squaring, easy, simply insert 0 between coefficients.
 - Example $110101 \rightarrow 10100010001$
- Multiplication <u>artificially hard</u> as instruction sets do not support "binary polynomial" multiplication, or "multiplication without carries" – which is actually simpler in hardware than integer multiplication! Really annoying!



- So we use Comb or Karatsuba methods...
- Squaring or multiplication results in a polynomial with 2m-1 coefficients.
- This must be *reduced* with respect to an irreducible polynomial, to yield a field element of *m* bits.

• For example for m=17, $x^{17}+x^5+1$



- This trinomial has no factors (irreducible)
- Reduction can be performed using shifts and XORs
- Example reduce
- 101001010101010101010101



1010010101010110101010101 $10000000000100001 \oplus$ $00100101010110000110101 \leftarrow$ 100101010110000110101 $10000000000100001 \oplus$ $000101010110100111101 \leftarrow$ 101010110100111101 $10000000000100001 \oplus$ $001010110100011100 \leftarrow$ $1010110100011100 \rightarrow result!$



Reduction in software - 1

- Consider the standard pentanomial x¹⁶³+x⁷+x⁶+x³+1
- Assume value to be reduced is represented as 11 32-bit words g[.]
- To be reduced to 6 word result
- In software the unrolled reduction algorithm looks like this



Reduction in software - 2

- $^{\circ} \quad g10 \leftarrow g[10], g9 \leftarrow g[9], g8 \leftarrow g[8], g7 \leftarrow g[7], g6 \leftarrow g[6]$
- $\circ \quad g[10] \leftarrow g[9] \leftarrow g[8] \leftarrow g[7] \leftarrow g[6] \leftarrow 0$
- $\circ g[5] \leftarrow g[5] \oplus (g10 \ll 4) \oplus (g10 \ll 3) \oplus g10 \oplus (g9 \gg 28) \oplus (g9 \gg 29)$
- $\circ \quad g[4] \leftarrow g[4] \oplus (g10 \ll 29) \oplus (g9 \gg 3) \oplus (g9 \ll 4) \oplus (g9 \ll 3) \oplus g9 \oplus (g8 \gg 28) \oplus (g8 \gg 29)$
- $\circ g[3] \leftarrow g[3] \oplus (g9 \ll 29) \oplus (g8 \gg 3) \oplus (g8 \ll 4) \oplus (g8 \ll 3) \oplus g8 \oplus (g7 \gg 28) \oplus (g7 \gg 29)$
- ° $g[2] \leftarrow g[2] ⊕ (g8 < 29) ⊕ (g7 > 3) ⊕ (g7 < 4) ⊕ (g7 < 3) ⊕ g7 ⊕ (g6 > 28) ⊕ (g6 > 29)$
- $\circ \quad g[1] \leftarrow g[1] \oplus (g7 \approx 29) \oplus (g6 \approx 3) \oplus (g6 \ll 4) \oplus (g6 \ll 3) \oplus g6$
- ° $g[0] \leftarrow g[0] ⊕ (g6 «29)$
- ° t ← g[5]»3, g[0] ← g[0]⊕t, t ← (t«3)
- ° $g[1] \leftarrow g[1] ⊕(t \approx 28) ⊕(t \approx 29)$
- ° $g[0] \leftarrow g[0] \oplus t \oplus (t \ll 4) \oplus (t \ll 3)$
- ° g[5] ← g[5] ⊕t



O 38 XORs, 33 shifts

Reduction in software - 3

- The shift values are
- S=163 mod 32 and 32-S = (3,29)
- S=163-7 mod 32 and 32-S = (28,4)
- S=163-6 mod 32 and 32-S = (29,3)
- S=163-3 mod 32 and 32-S = (0,32) (!!)
- A shift by 32 results in a zero, and a shift by 0 is free. What if the irreducible polynomial was chosen to make this happen more often? Saves 1 XOR and 2 shifts per line...



- Now try $x^{163} + x^{99} + x^{97} + x^3 + 1$
- Note that this time the shifts are by (3,29), (0,32), (2,30) and (0,32)
- Should result in shorter, faster reduction code.
- It does...



- $\circ \quad g10 \leftarrow g[10], g9 \leftarrow g[9], g8 \leftarrow g[8], g7 \leftarrow g[7], g6 \leftarrow g[6]$
- $\circ \quad g[10] \leftarrow g[9] \leftarrow g[8] \leftarrow g[7] \leftarrow g[6] \leftarrow 0$
- $g8 \leftarrow g8 \oplus g10 \oplus (g10 \gg 2), g7 \leftarrow g7 \oplus (g10 \ll 30) \oplus g9 \oplus (g9 \gg 2)$
- ° $g6 \leftarrow g6 ⊕ (g9 \ll 30) ⊕ g8 ⊕ (g8 \gg 2)$
- ° $g[5] \leftarrow g[5] ⊕ g10 ⊕ (g8 « 30) ⊕ g7 ⊕ (g7 » 2)$
- ° $g[4] \leftarrow g[4] ⊕ (g10 \ll 29) ⊕ (g9 \gg 3) ⊕ g9 ⊕ (g7 \ll 30) ⊕ g6 ⊕ (g6 \gg 2)$
- ° $g[3] \leftarrow g[3] ⊕ (g9 < 29) ⊕ (g8 > 3) ⊕ g8 ⊕ (g6 < 30)$
- ° $g[2] \leftarrow g[2] ⊕ (g8 \approx 29) ⊕ (g7 \approx 3) ⊕ g7$
- ° $g[1] \leftarrow g[1] ⊕ (g7 \approx 29) ⊕ (g6 \approx 3) ⊕ g6$
- ° $g[0] \leftarrow g[0] ⊕(g6 < 29)$

- $g[0] \leftarrow g[0] \oplus t$
- ° g[2] ← g[2]⊕(t«30)
- $g[3] \leftarrow g[3] \oplus t \oplus (t \approx 2)$
- ° g[5] ← g[5] \oplus t

O 35 XORs and 23 shifts



- If the irreducible trinomial is of the form x^m+x^a+1, and (m-a) is a multiple of the word-length, then it's a *lucky* trinomial (LT).
- If the irreducible pentanomial is of the form x^m+x^a+x^b+x^c+1, and (ma), (m-b), (m-c) are all multiples of the word-length, then it's a *lucky* pentanomial (LP).



- If only two out of three of (*m-a*), (*m-b*) and (*m-c*) are multiples of the wordlength, then it's a *fortunate* pentanomial (FP).
- LTs may not exist or very rare.
- LPs can be quite plentiful
- FPs are even more plentiful
- Clearly helps if a (and b and c for a pentanomial) are all odd.



Square roots

- As it happens having, a (and b and c) odd is already a good idea, as it facilitates a much faster square rooting algorithm (Fong et al.)
- Square roots are important for point-halving algorithms, and for the η_T pairing



Square roots

- O Assume trinomial
- Let $\zeta = x^{(m+1)/2} + x^{(a+1)/2}$
- Then $\sqrt{a} = a_{even} + \zeta a_{odd}$
- Where a_{even} are the even indexed elements of a collapsed into a half sized bit array, and a_{odd} are the odd indexed elements of a also collapsed into a half sized bit array.



Example

- In GF(2¹⁷) find square root of
 01010110100011100
- $\circ a_{even} = 000110110$
- $\circ a_{odd} = 011100010$

$$\circ \zeta = x^9 + x^3$$

O So the square root is...





00000000000110110 011100010000000000 ⊕ 00000011100010000 ⊕

011100001100100110

No reduction required!



Square roots

- In software it helps if (m+1)/2 and (a+1)/2 are multiples of the word length – again less shifts will be needed.
- No reason not to insist on trinomials/pentanomials with odd a (b and c)



Some bad news..

- If a trinomial does not exist for the given field, a lucky pentanomial does not exist either (Buhler)
- If m=±1 mod 8 a trinomial might be found, and so might a lucky pentanomial
- If m = ± 3 mod 8, no trinomial, no lucky pentanomial, but maybe a fortunate pentanomial.



Pecking order..

- A lucky trinomial beats a lucky pentanomial, beats an ordinary trinomial, beats a fortunate pentanomial.
- But for $m = \pm 3 \mod 8$ only hope is a fortunate pentanomial \otimes
- So pentanomial can be better than a trinomial – Yes!



Pentanomial beats a trinomial?

- O Consider GF(2²³³)
- Trinomial x²³³+x¹⁵⁹+1
- Lucky pentanomial for 32-bit processor x²³³+x²⁰¹+x¹⁰⁵+x⁹+1
- Trinomial requires 4 XORs and 4 shifts per iteration of the (rolled) reduction algorithm
- Pentanomial requires 5 XORs and only 2 shifts



Code for GF(2²³³) trinomial



Code for GF(2²³³) pentanomial

```
for (i=xl-1;i>=8;i--)
ł
  w=gx[i]; gx[i]=0;
  gx[i-1]^=w;
  qx[i-4]^=w;
  gx[i-7]^{=}(w>>9)^{w};
  qx[i-8]^{=}(w<<23);
} /* XORs= 5 shifts= 2 */
top=gx[7]>>9; gx[0]^=top; top<<=9;
gx[0]^=top;
gx[3]^=top;
gx[6]^=top;
qx[7]^=top;
```



Pentanomial beats a trinomial?

- OBUT WAIT. ON THE ARM PROCESSOR shifts are free!
- EOR R1,R2,R3,LSL #7
- ° R1=R2⊕(R3≪7)
- Very nice feature for GF(2^m) arithmetic!
- But not supported on most other architectures!



Pentanomial beats a trinomial?

- In fact on smaller processors shifts may be <u>much</u> more expensive than XOR.
- May only support 1-bit shifts and rotates, so multi-bit shifts require multiple clock cycles.
- On MIPS/Pentium a shift (of any length) costs same as XOR
- So in many (most?) cases a lucky pentanomial beats a trinomial.



Real-world architectures

- O Texas Instruments msp430
- O 16-bit processor
- One bit shifts/rotates only
- Our Used in Wireless Sensor Networks
- O Atmel Atmega-128
- 8-bit processor
- One bit shifts/rotates only



What about m=±3 mod 8?

- O Maybe try to find a lucky redundant pentanomial (Brent & Zimmerman)?
- For m=163, $x^{165}+x^{69}+x^{37}+x^{5}+1$ has 163 degree factor.
- Are redundant polynomials a good idea??



Results

- Using a simple cost model which costs XORs and shifts appropriately for a selection of architectures, we search for the optimal polynomials, for standard values of *m*.
- For different word length, often different results (not surprising), but also often there is agreement



Results

- In many cases a lucky pentanomial is better than a trinomial
- For msp430 processor, pentanomial is always superior.
- For Atmel 8-bit processor lucky trinomials are possible.



Last words

- The current standard polynomials should be scrapped! They are awful!
- Replacements should be square root friendly, and close to optimal for a wide range of architectures.



Optimal Irreducible Polynomials

Questions??

